

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10773948
	Filing Date		2004-02-06
	First Named Inventor	S. Babar Raza	
	Art Unit	2416	2473
	Examiner Name	Hahn N. Nguyen	
	Attorney Docket Number	CD02209	

U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/H.N./	1	5898687	B1	1999-04-27	Harriman et al.	Entire Document
	2	5805808	B1	1998-09-08	Hasani et al.	Entire Document
	3	6430626	B1	2002-08-06	Witkowski et al.	Entire Document
	4	6212597	B1	2001-04-03	Conlin et al.	Entire Document
	5	5586294	B1	1996-12-17	Goodwin et al.	Entire Document
	6	6115379	B1	2000-09-05	Flanders et al.	Entire Document
	7	5502833	B1	1996-03-26	Byrn et al.	Entire Document
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/H.N./	9	6515991	B1	2003-02-04	McKeown, Nicholas W.	Entire Document
	10	6131123	B1	2000-10-10	Hurst et al.	Entire Document
	11	6163810	B1	2000-12-19	Bhagavath et al.	Entire Document
	12	6246682	B1	2001-06-12	Roy et al.	Entire Document
	13	6295295	B1	2001-09-25	Wicklund, Goran	Entire Document
	14	5991295	B1	1999-11-23	Tout et al.	Entire Document
	15	5418781	B1	1995-05-23	Kaufman et al.	Entire Document
	16	5970229	B1	1999-10-19	Thomas et al.	Entire Document
	17	6934289	B1	2005-08-23	Lee, Jong lck	Entire Document
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/H.N./	1	20020091916	A1	2002-07-11	Dowling, Eric M.	Entire Document

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FOREIGN PATENT DOCUMENTS

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/H.N./	1	Application Number 10/746,349 (CD02208): "Logic for Implementing a Dual Clock Domain Read Access with Predictable timing for Bi-Directional Inputs/Outputs;" Raza; 31 pages	<input type="checkbox"/>
/H.N./	2	Provisional Application 60/445,872 (CD02208P): "Logic for Implementing a Dual Clock Domain Read Access with Predictable timing for Bi-Directional Inputs/Outputs;" Raza; 6 pages	<input type="checkbox"/>
/H.N./	3	Provisional Application 60/437,303 (CD02209P): "Dual-Port Memory Using a Single Port Memory for Multiple Clock Domain;" Raza; 9 pages	<input type="checkbox"/>
/H.N./	4	USPTO Notice of Allowance for Application Number 09/676,706 (CD00101) dated 10/19/2005; 6 pages	<input type="checkbox"/>

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/H.N./	5	USPTO Advisory Action for Application Number 09/676,706 (CD00101) dated 08/10/2005; 2 pages	<input type="checkbox"/>
	6	USPTO Final Rejection for Application Number 09/676,706 (CD00101) dated 06/01/2005; 7 pages	<input type="checkbox"/>
	7	USPTO Non-Final Rejection for Application Number 09/676,706 (CD00101) dated 09/22/2004; 7 pages	<input type="checkbox"/>
	8	S. Babar Raza et al., "Architecture for Implementing Virtual Multiqueue Fifos;" Application Number 09/676,704; filed 09/29/2000; 79 pages	<input type="checkbox"/>
	9	S. Babar Raza et al., "Logic for Providing Arbitration for Synchronous Dual-Port Memory;" Application Number 09/676,169; filed 09/29/2000; 82 pages	<input type="checkbox"/>
	10	S. Babar Raza et al., "Logic for Initializing the Depth of the Queue Pointer Memory;" Application Number 09/676,705; filed 09/29/2000; 82 pages	<input type="checkbox"/>
	11	S. Babar Raza et al., "Method and Logic for Initializing the Forward-Pointer Memory During Normal Operation of the Device as a Background Process;" Application Number 09/676,170; filed 09/29/2000; 82 pages	<input type="checkbox"/>
	12	S. Babar Raza et al., "Method for Logic for Storing and Extracting in-Band Multicast Port Information Stored along with the Data in a Single Memory without Memory Read Cycle Overhead;" Application Number 09/676,171; filed 09/29/2000; 83 pages	<input type="checkbox"/>
/H.N./	13	S. Babar Raza et al., "Logic for Generating Multicast/Unicast Address(es);" Application Number 09/676,706; filed 09/29/2000; 83 pages	<input type="checkbox"/>

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EXAMINER SIGNATURE

Examiner Signature	/Hanh Nguyen/	Date Considered	11/18/2009
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

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☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

- ☐ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☒ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Lucinda G. Price/	Date (YYYY-MM-DD)	2009-05-26
Name/Print	Lucinda G. Price	Registration Number	42270

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